## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: PRITCHARD et al. Attorney Docket No.:

03-2051/LSI1P240

Application No.: 10/791,337

Examiner: TRINH, Michael M.

Filed: March 1, 2004

Group: 2822

Title: SPACER-LESS TRANSISTOR

INTEGRATION SCHEME FOR HIGH-K GATE DIELECTRICS AND SMALL GATE-TO-GATE

SPACES APPLICABLE TO Si, SiGe STRAINED

SILICON SCHEMES

## CERTIFICATE OF EFS-WEB TRANSMISSION

I hereby certify that this correspondence is being transmitted electronically through EFS-WEB to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on **December 20, 2007**.

Confirmation No.: 2401

Signed: /Lydie Fitzsimmons/
Lydie Fitzsimmons

## PETITION AND FEE FOR EXTENSION OF TIME TO MAINTAIN PARENT CASE WHICH IS TO BE ABANDONED WHEN FILING NEW APPLICATION CLAIMING ITS BENEFIT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 27, 2007, Applicant petitions for an extension of time under 37 CFR 1.136(a) for the total number of months checked below:

<u>Months</u>	<u>Large Entity</u>	Small Entity
one	\$120.00	\$60.00
two	\$460.00	\$230.00
three	\$1,050.00	\$525.00
four	\$1,640.00	\$820.00

If an additional extension of time is required, please consider this a petition therefor.

The Commissioner is hereby authorized to charge \$120.00 in payment of the Petition for a one Month Extension. fee and any additional fees, including any extension fees, which may be required or credit any overpayment directly to the account of the undersigned, No. 12-2252 (Order No. 03-2051/LSI1P240).

Respectfully submitted, BEYER WEAVER LLP

/Francis T. Kalinski II/ Francis T. Kalinski II Reg. No. 44,177

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